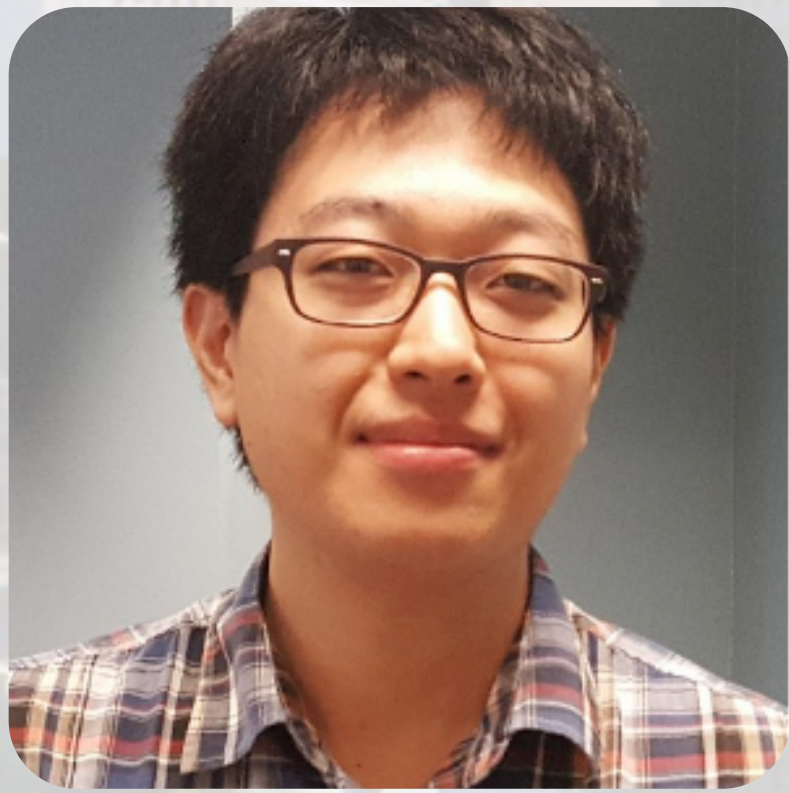


Zoom Link via QR code



**Seminar Announcement  
Columbia University Electrical  
Engineering Department**

**Professor Wanyeong Jung**

***KAIST, Korea***

**Date:** Monday, October 28

**Time:** 10:00am-11:00am

**Location:** EE Conference Room

**Title:** Rethinking Circuit Design:  
Strategies for Improving Efficiency

*Electrical Engineering Seminar Series*

**Rethinking Circuit Design: Strategies for Improving Efficiency**

Traditional circuit development often involves significant design overhead, including extensive iterations to meet performance targets under varying conditions. Finding new circuit topologies to meet challenging design targets has become increasingly complex, with additional burdens in optimizing each component. This seminar discusses strategies for improving efficiency in circuit design by addressing these challenges. I will introduce design approaches and outcomes recently explored by my research team, focusing on analog and mixed-signal circuits based on simple and reliable mechanisms such as inverters and logic gates, as well as automation tools for generating and testing circuit topologies.

**About Professor Wanyeong Jung**

Prof. Wanyeong Jung received his Ph.D. in Electrical Engineering from the University of Michigan, Ann Arbor, in 2017. He is currently a faculty member in the School of Electrical Engineering at the Korea Advanced Institute of Science and Technology (KAIST), Daejeon, South Korea. His research interests include low-power circuits and systems, energy-efficient edge computing, and the Internet of Things (IoT).